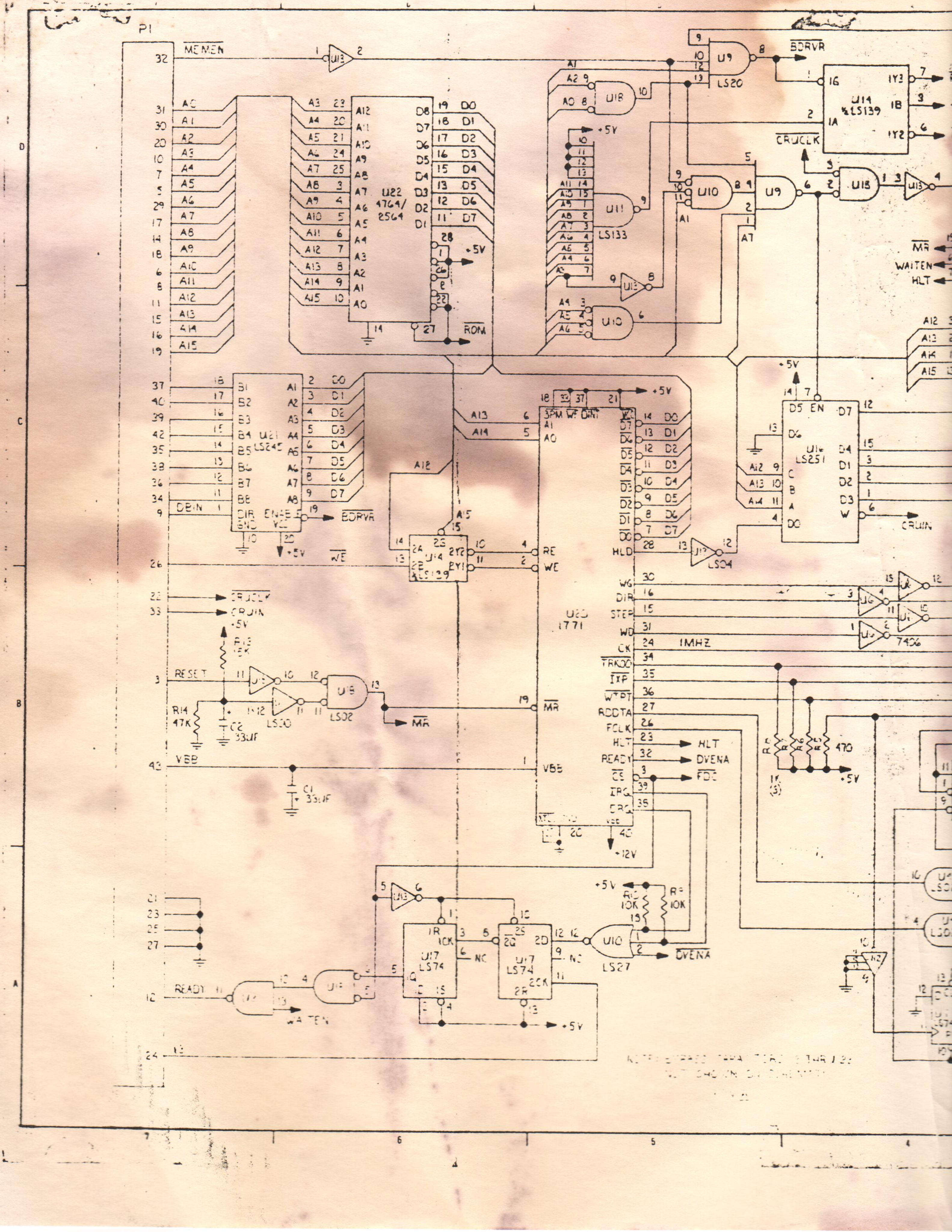
P1
MEMEN
BDRVR
U9 LS20
U18
U14 ½LS139
1Y3
1B
1Y2
1A
1G
CRUCLK
U11 LS133
U10
U13
+5V
U22 4764/2564
ROM
A12
A13
A14
A15
U21 LS245
DBIN
DIR
ENABLE
GND
VCC
WE
U14 LS139
2Y2
2Y1
RE
U23 1771
3PM
DINT
HLD
WG
DIR
STEP
WD
CK
1MHZ
TRK00
IXP
WPRT
RDDTA
FCLK
HLT
READY
DVENA
CS
FDC
IRQ
DRQ
MR
VBB
+12V
U16 LS251
D5
EN
D7
D6
D4
D1
D2
D3
W
D0
CRUIN
LS04
7406
470
1K (3)
WAITEN
RESET
R13 15K
R14 47K
C2 33UF
C1 33UF
LS00
LS02
U15
U17 LS74
NC
R10 10K
R9 10K
LS27
READY
WAITEN
D0
D1
D2
D3
D4
D5
D6
D7
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
7
6
5
4
D
C
B
A